2x2 Binary Muliplier:

A diagram of a multiplier

Description automatically generated

3x3 Binary Multiplier:

A screenshot of a computer

Description automatically generated

Half Adder:

A diagram of a circuit

Description automatically generated

Full Adder:

A diagram of a circuit

Description automatically generatedA table with numbers and symbols

Description automatically generated

Half Subtractor:

A diagram of a circuit diagram

Description automatically generatedA table with numbers and a difference

Description automatically generated

Full Subtractor:

A diagram of a circuit

Description automatically generatedA table with numbers and words

Description automatically generated

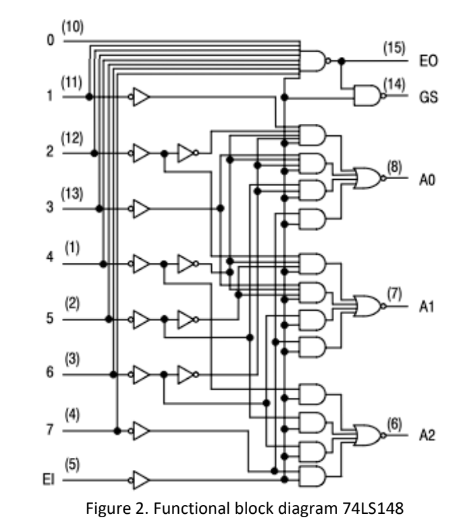
one-bit comparator:

A diagram of a circuit

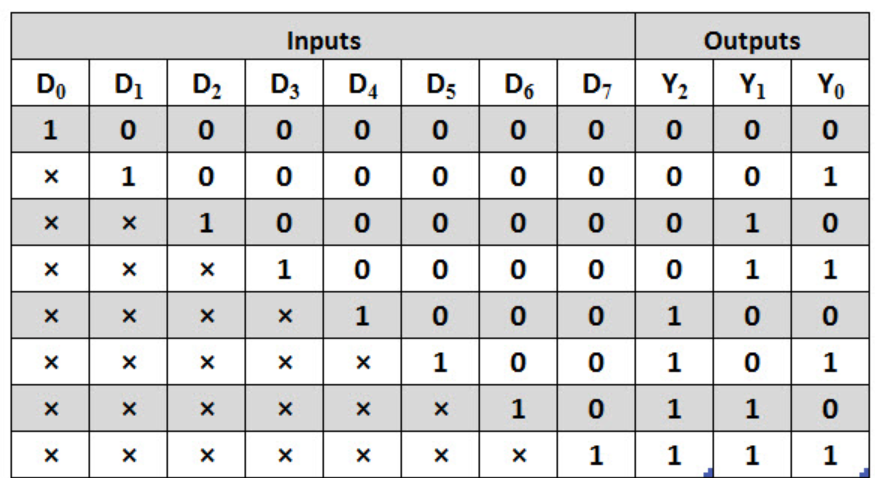
Description automatically generatedA diagram of a circuit

Description automatically generated

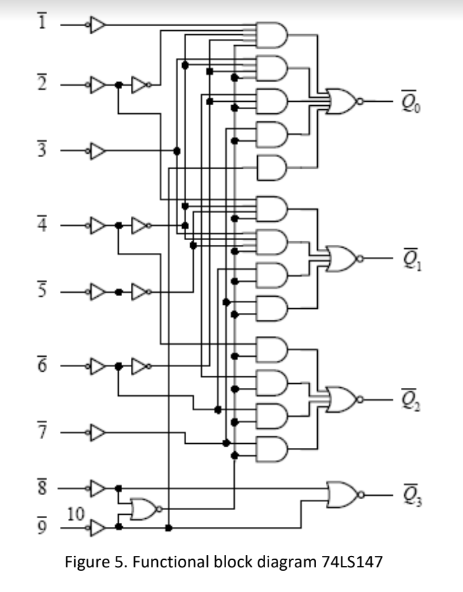
74148 (8 x 3) Octal to Binary Priority Encoder:

A diagram of a computer input output

Description automatically generated



Decimal to BCD Priority Encoder (74147):

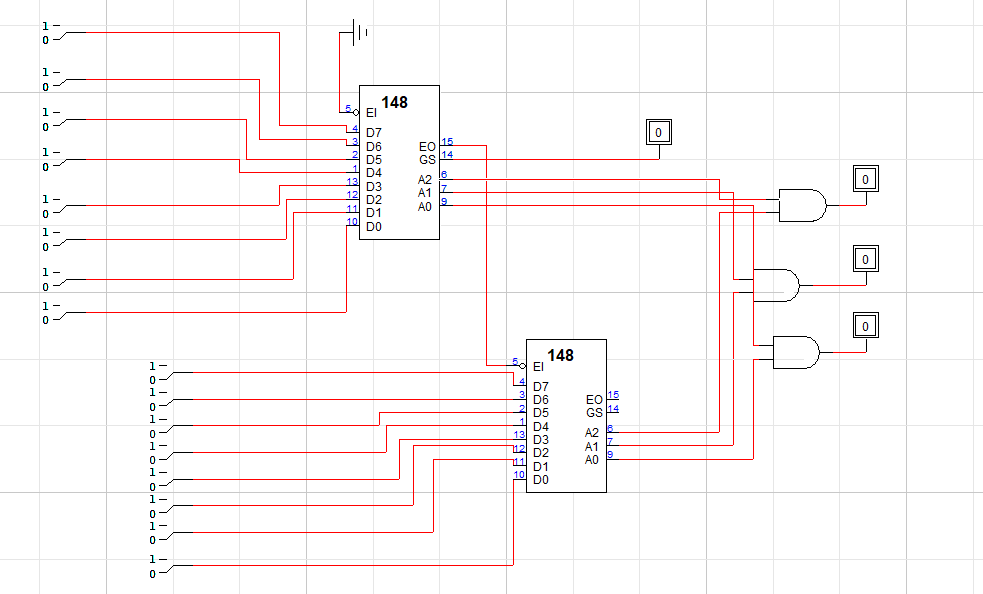
A diagram of a computer input output

Description automatically generated

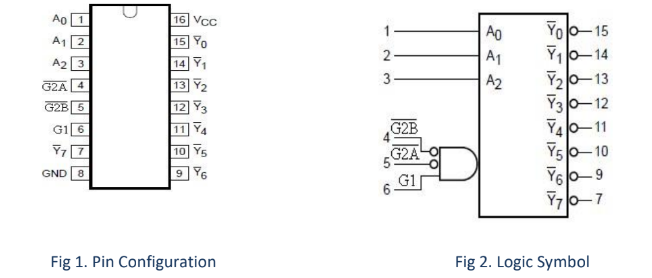
A table of numbers and symbols

Description automatically generated

16 bit Octal to Binary Priority Encoder using 74148 IC.:



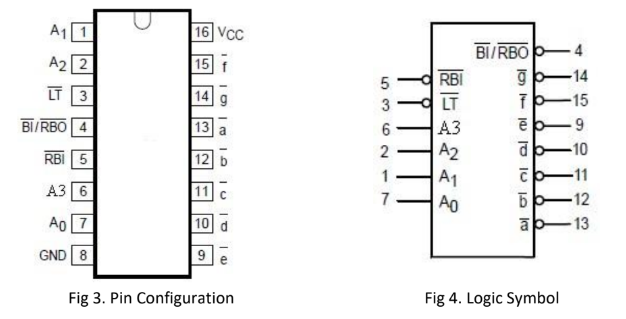
Implementation of 3-8-line Decoder using IC 74LS138:



A table with text on it

Description automatically generated

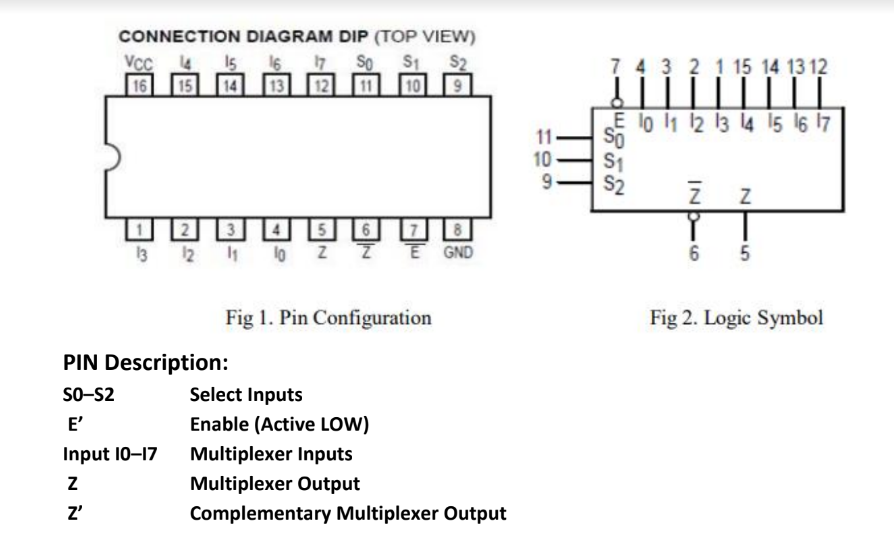
Implementation of BCD to Seven Segment Decoder using IC 74LS47:



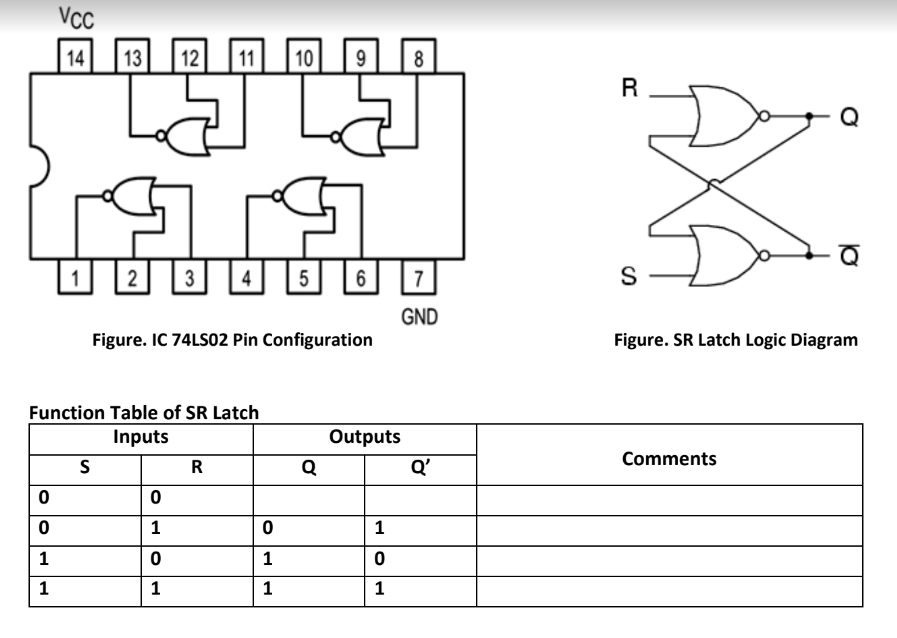
A table with text on it

Description automatically generated

8x1 Multiplexer:



SR Latch Using NOR Gate:



D Latch:

A diagram of a circuit

Description automatically generatedA table with black text

Description automatically generated

Edge Triggered D Flip-Flop:

A screenshot of a computer

Description automatically generated

JK Flip-Flop:

A diagram of a number and a flip flop

Description automatically generated

A diagram of a circuit

Description automatically generated